

## ABSTRACT OF THE DISCLOSURE

A low-power receiver architecture of use in direct sequence spread spectrum communications system receivers, such as in a code division multiple access system, is provided. The receiver employs analog correlation, wherein its analog-digital converters are able to operate at the system symbol rate, and the requirement for finite impulse response filters is obviated. The low-power receiver architecture is also applicable as a front-end structure for multiple-mode receivers.

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